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Lattice QCD on QPACE 4

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QPACE 4 is the latest member of the QCD PArallel Compute Engine (QPACE) series, which was deployed in Regensburg in June 2020. It features 64 Fujitsu A64FX model FX700 CPUs interconnected by InfiniBand EDR. The A64FX is the first CPU supporting the Arm Scalable Vector Extension (SVE). In this contribution we discuss the implementation of SVE in the Grid Lattice QCD framework and show Grid benchmarks on QPACE 4.

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