



Program

	<i>JST (UTC + 9) Japan Time</i>	<i>PST (UTC - 8) California Time</i>	<i>CET (UTC + 1) Central Europe Time</i>	
	22/Jan./2021 12:00	21/Jan./2021 19:00	22/Jan./2022 04:00	<i>Session Chair</i>
<i>Opening remarks</i>	12:30-12:40	19:30-19:40	4:30-4:40	<i>Hiroshi Kawata</i>
<i>key note lecture</i>	12:40-13:20	19:40-20:20	4:40-5:20	<i>Hidemi Ishiuchi</i>
	<i>Fundamental Research to Solve the EUV Technical Issues and Future Prospect for EUVL toward Advanced Devices Fabrication</i>			
<i>Invited speaker(1)</i>	13:20-13:50	20:20-20:50	5:20-5:50	<i>Ryoichi Hajima</i>
	<i>Compact Storage Ring FEL: a kW-scale EUV lithography source.</i>			
<i>Invited speaker(2)</i>	13:50-14:20	20:50-21:20	5:50-6:20	<i>Ryoichi Hajima</i>
	<i>Construction and commissioning of cERL IR-FEL toward realization of the EUV-FEL</i>			
<i>Invited speaker(3)</i>	14:20-14:50	21:20-21:50	6:20-6:50	<i>Ryoichi Hajima</i>
	<i>Industrial application of accelerator : Medical RI production</i>			
<i>Break</i>				<i>Break</i>
<i>Invited speaker(4)</i>	15:20-15:50	22:20-22:50	7:20-7:50	<i>Soichi Inoue</i>
	<i>Driving Moore' s Law through Process and Packaging Innovations</i>			
<i>Invited speaker(5)</i>	15:50-16:20	22:50-23:20	7:50-8:20	<i>Soichi Inoue</i>
	<i>EUV Lithography as key scaling enabler for logic and memory</i>			
<i>Invited speaker(6)</i>	16:20-16:50	23:20-23:50	8:20-8:50	<i>Soichi Inoue</i>
	<i>High-NA EUV Lithography exposure tool for EUV roadmap extension</i>			
<i>Closing address & Photo-session</i>	16:50-17:00	23:50-24:00	8:50-9:00	<i>Hiroshi Kawata</i>

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