MTCA workshop for accelerator and physics in Japan 2021 (Virtual)

# Survey on MTCA in Japanese accelerators

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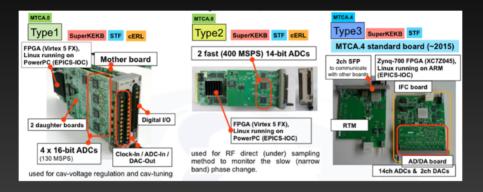
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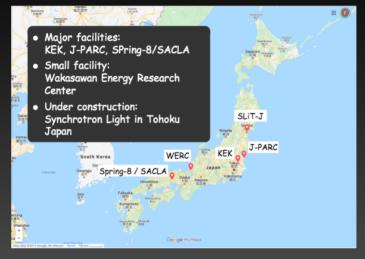
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#### Introduction



Application of MTCA for accelerators was pioneered by Prof. Shin Michizono (KEK) in 2008.

## Since then, MTCA is spreading to Japanese accelerators



However, it seems slow. Why?  $\rightarrow$  Survey!

## I was inspired by...

Comprehensive survey for MTCA users of accelerators in the world by Daniel Tavares (LNLS). IBIC2019:

https://doi.org/10.18429/JACoW-IBIC2019-WEB002





MicroTCA 4 at Sirius and a closer look into the community

IRIC 2019 - Malmi

Daniel Tayares LNLS Beam Diagnostics Group

September 11, 2019

White Bear Seston Cod MICCORD Makes Sender TACOM BARRAGA W ISBN: 978. 3.95450. 204. 2 BSSN- 2623-5358 pol:10.18429/JACov-18IC2019-WE8002

Table 1: MTCA 4 aminos on accelerator facilities (non-exhaustive list), a) LLRE h) RPM Electronics, c) RAM Electronics of Beam Diagnostics (other than RPM and BAM Electronics), e) Symptomization/Dimine () Machine Protection, e) English Control b) Incom Reconsider i) Experienced Control i) Marrian Data Reconsider

Facility	Location		ь	¢	d	e	ſ	8	h	i	j	Number of Crates
DESY (E-XFEL, FLASH) [39]	Germany	х	х	х	х	х	х	х	х	х	х	200+
ESS [40, 41]	Sweden	×	х		×	х	х					101-200
ORNL (SNS) [42]	USA	×	-		-	-	х	×		-	х	101-200
GSI (FAIR) [43]	Germany	×	х		×	х		×	х	х		51-100
Spring-8/SACLA [44,45]	Japan	×	х		×	х		×		-		51-100
CERN (SPS) [46]	Switzerland	×				х		×		х		21-50
FRIB [47]	USA		х		×	х	х					21-50
LNLS (Sirius) [20]	Beazil	×	х			х		×		-		21-50
APS-U [48]	USA	×			×			×				21-50
IHEP (HEPS)	China	×				х	х					21-50
ELI Beamlines [49]	Czech Republic					х				х		21-50
PAL (PAL-XFEL)	South Korea		х									21-50
CSNS (IHEP)	China	×										11-20
Diamond [50]	UK	×			×			×				6-10
KEK (SuperKEKB, STF-2) [51]	Japan	×	x			-						6-10
SINAP (SXFEL, SHINE) [52]	China	×										6-10
KIT (FLUTE) [53]	Germany	×	х		×	х		×		х		1-5
CANDLE [54]	Armenia	×			×	х						1-5
Soleil	France	×							×	×		1-5
USTC (HLS-II)	China	×	х									1-5
HZDR (ELBE) [55]	Germany	×				х						1-5
ANSTO (AS) [56]	Australia						х					1-5
Elettra	Italy							×				1-5
ESRF	France							×				1-5
IMP/CAS (ADS) [57]	China	×										1-5
J-PARC [58]	Japan	×										1-5
JGU (MESA (591)	Germany	×										1-5

#### TRENDS FOR BEAM DIAGNOSTICS

BPM Flactronics

MicroTCA 4 was been targeting a wide range of frequencies for analog signals processing and digitizing, from DC to a few GHz. With the extension of an auxiliary backglane It in the MicroTCA 4.1 revision, clean LO and reference clock. signals as required in analog down- and apconversion were made quallable. All of these building blocks make it possible to implement BPM electronics in a very compact crate setup.

Different design approaches were taken across the community. For instance, PAL-XFEL designed a custom RTM BPM in partnership with SLAC [60], used in conjunction with Struck SIS\$300 AMC digitizer. A similar approach was followed by DESY for FLASH low charge RPMs, for which a custom RTM BPM electronics was designed [61]. but so far no request has been made by clients [63]. Spring-S and HEPS have adopted the same architecture using malog AMCs, but having designed in house both ASE. "Wastibusen Perublack

At Sirius (LNLS) (20) and CRYRING (GSD [43] a third approach was taken: instead of RTM analog front-ends or digitizers such as the typical use case of MicroTCA.4, FMC ADC boards were used in AMC FMC carriers. Siznal conditioning electronics resides outside the crate, in dedicated

ESS BPM electronics design employed both COTS AMC digitizer and RTM down-conversion electronics. All BPMspecific analog signal conditioning is done in a separate electronics outside the crate [62].

Libera Brilliancea (L'Tech) uses a distinct approach from all others by placing all analog and FPGA digital processing for one BPM in one single AMC. The AMC module is MicroTCA (3-compliant only, since the hardware architecture was decided before the release of MicroTCA 4. Customization to make a MTCA & AMC module would be possible 1. Introduction

## 2. Survey on MTCA in Japanese accelerators

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## The survey form



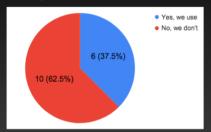
Thanks to Google forms, it took only a few hours to prepare a small survey. The survey was performed in Summer 2020.

#### Disclaimer

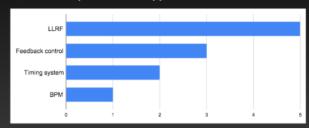
The survey was answered by people working for LLRF, beam monitor, and control system of KEK and J-PARC.

- The scope of the survey is limited to my related people, so it is not an accurate re ection of the actual situation in Japan
- Therefore, the results of the survey are not suitable for statistical treatment
- Analysis can be biased

#### Q: Do you use MTCA?

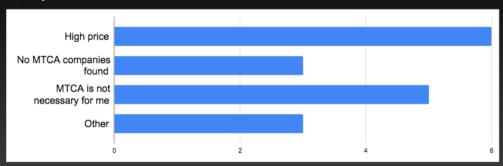


## Q: What is your MTCA application?



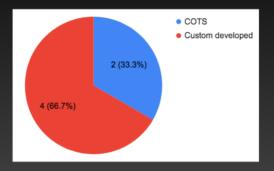
- Most of Yes answers came from LLRF people
- Main application is LLRF, similar to the institutes in the world
  - Others: feedback control, timing system, and BPM

Q: If you do not use MTCA, what is the reason?



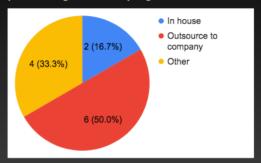
- Main reasons: High price and less need
- Cannot nd the companies that can develop MTCA systems
- Other: no community to share knowhow of MTCA, and high initial barrier to start MTCA development

Q: Do you develop your custom MTCA modules or buy COTS modules?



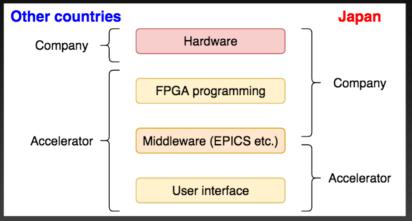
• COTS vs custom development = 2:4

Q: How do you develop FPGA logic or DSP programs?



- Only few answers of In house
- Other contains:
  - Prototyping is done in house but implementation to real machine is by company
  - Functionality is designed by institute and implementation is by company
- In most cases it is outsourced to companies

## Difference of development style



- FPGA logic is usually outsourced in Japan
  - Less engineer/technician in institutes
- Role of companies is more than other countries

Q: What are the pros and cons of MTCA in your opinion?



#### Pros:

- High-performance AMCs and high speed backplane are available
- Module management, hot-swap, redundancy
- Promising future
- (About MTCA.4) AMC/RTM con guration allows separation of analog and digital circuits

Q: What are the pros and cons of MTCA in your opinion?



#### Cons:

- High price. There are not many companies that can develop MTCA in Japan (Many same answers)
- Fewer COTS products
- Dif cult module design due to the need to equip an MMC (Module Management Controller) for module management
- Interoperability. Each manufacturer of MCHs has its own compatibility with MMCs, and the system needs to be checked to see if it works in each con guration
- There is no data sharing mechanism for all AMCs, like VME bus or PLC shared memory

Q: What do you consider to be necessary for MTCA to become popular in Japan?

- Increase in the number of companies that can develop modules in Japan
- Lower price
- More promotion

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Although the survey was conducted within a narrow scope, the results highlight barriers to the spread of MTCA in Japan.

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## Barriers to the spread of MTCA in Japan

#### (1) High price:

- Common issue in the world
- MTCA shelf vendors seem to be making effort against the issue
  - Smaller shelves with MCH and/or CPU
- Japanese development style (FPGA program outsourced)
  - Buying COTS AMC and programming in house may reduce cost; hire / grow young staff
- High performance (in most cases) requires high cost
  - Example: J-PARC new LLRF is expensive but has wonderful performance
  - Management needs to change its mindset. Control system is as important as big power supply or klystron







## Barriers to the spread of MTCA in Japan

- (2) There are only a few companies in Japan that can manufacture MTCA modules:
  - MTCA is complicated. Module management requires implementation of micro controller on the modules
    - Hopefully DMMC-stamp will help
  - Japanese companies, which made high-quality VME or cPCI modules, should be able to develop MTCA modules as well
    - If they get enough information

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It is necessary to establish MTCA community in Japan to share information and know-how in order to promote the spread of MTCA in Japan.

### Summary

A small survey on MTCA in Japanese accelerators, KEK and J-PARC, was conducted.

Indeed, the motivation to organize this workshop is presented here.

 We should establish MTCA community in Japan to share information and know-how in order to promote the spread of MTCA in Japan

## Thank you for your attention!



Questions? → fumihiko.tamura@j-parc.jp