



# The crucial role of the **Software Interlock System**

at CERN's  
Super Proton Synchrotron

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CERN – SPS Operations

- **Introduction**
  - CERN injectors chain
  - SPS complex
- **Machine Protection**
  - Damage
  - Machine Protection Synergy
- **Beam Interlock System (BIS)**
  - Beam Interlock Controller (BIC)
- **Software Interlock System (SIS)**
  - SIS - Graphical User Interface (GUI)
  - Used as diagnostic
  - Mask and Latch
  - Configuration
- **Summary**

## CERN injectors chain:

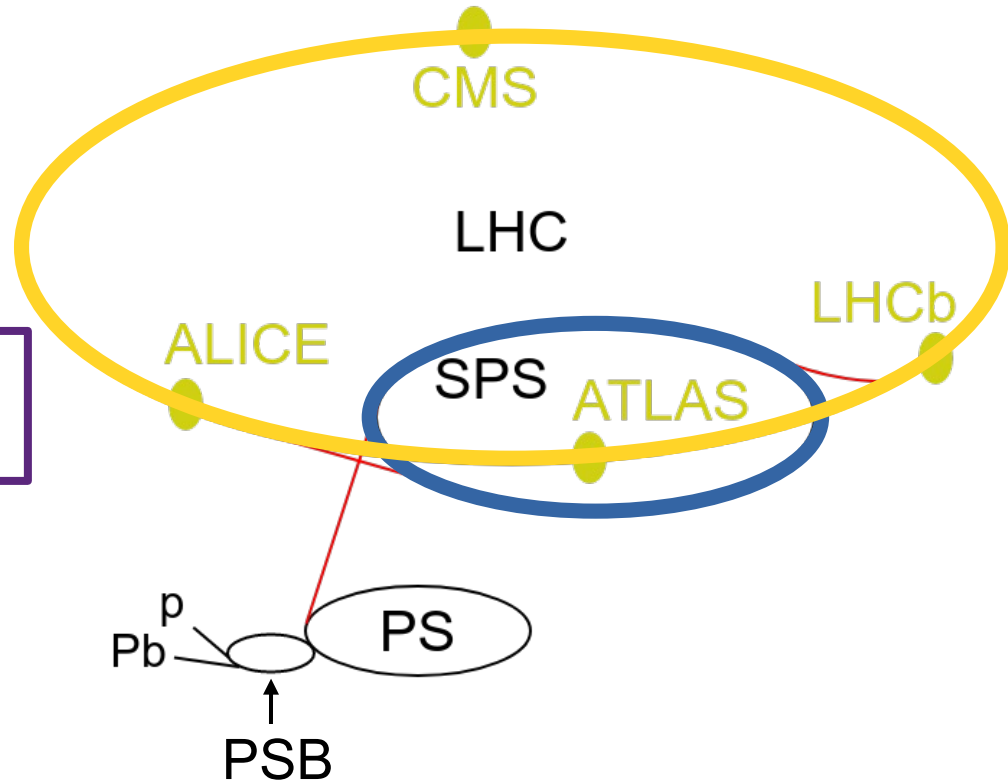
The accelerated particle types are protons and heavy ions

Energy:

- LHC: 6.8 TeV/c
- SPS: 450 GeV/c
- PS: 26 GeV/c
- PSB: 1.4 GeV/c

Intensity per beam:

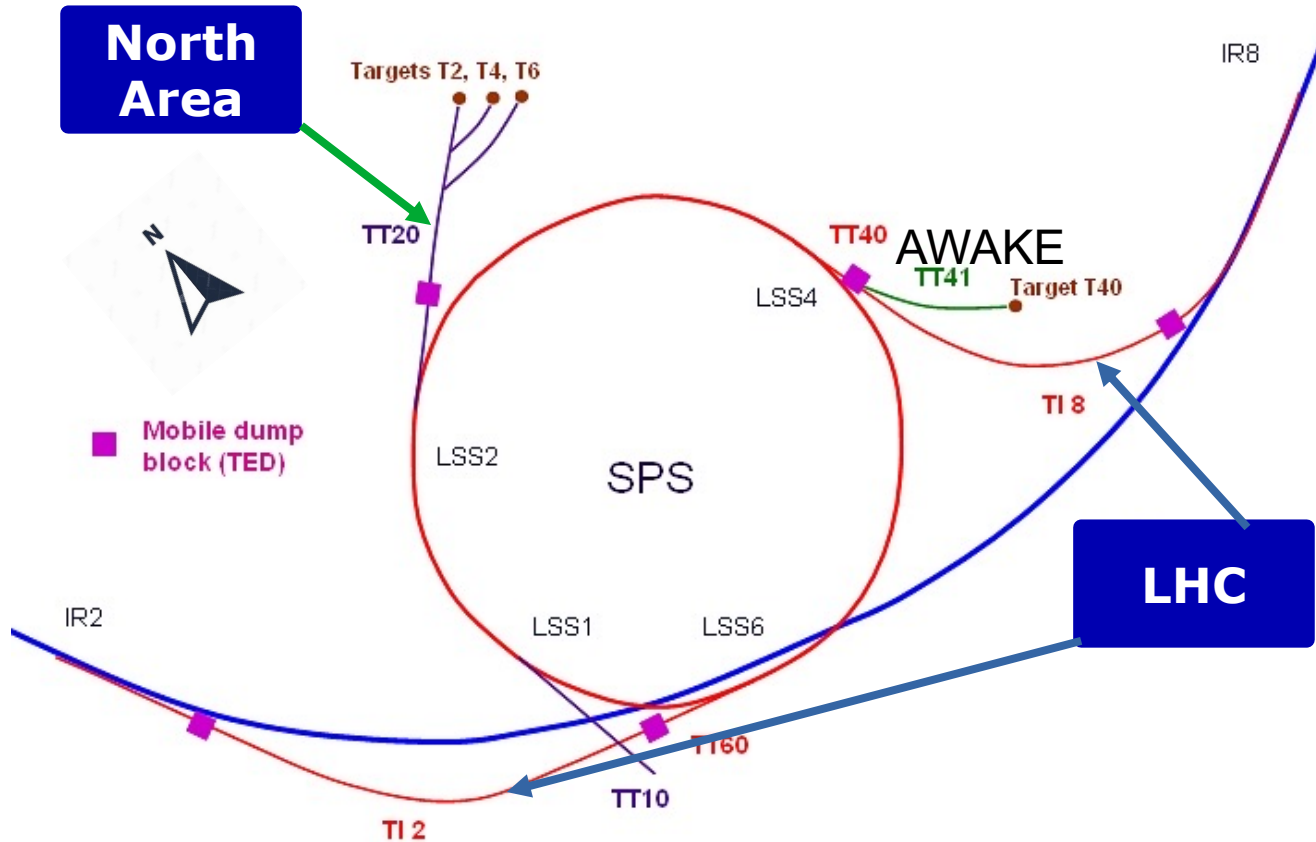
- LHC: > 35E13 p<sup>+</sup>
- SPS: > 4E13 p<sup>+</sup>
- PS: > 2E13 p<sup>+</sup>
- PSB: > 2E13 p<sup>+</sup>



Circumference:

- LHC: 26,659 m
- SPS: 6,912 m
- PS: 628 m
- PSB: 157 m

The SPS is the injector of the Large Hadron Collider (LHC).  
SPS is delivering beam to North Area to fulfill Fixed Target physics.  
Machine Development Studies are performed for optimisation of new beams.



- SPS and LHC beam energy can be above the damage level of accelerator components (vacuum chamber, magnets, etc...).

→ Protection by an interlock system:

"interlock" refers to a safety mechanism or control system designed to automatically halt the operation of the accelerator

- Designed to detect faults, failures and potentially dangerous situations
- To prevent excessive beam losses
- To stop operation when something goes wrong  
Caveat: a trade-off between protection and beam availability
- To help diagnose and resolve problems
- To check the machine configuration

# Damage

Accidental high intensity ( $4E13$  p<sup>+</sup>) Fixed Target beam crash at 400GeV/c :



## Beam Interlock System (BIS):

- entirely implemented in **Hardware**
- dumps the beam
- prevents injection or extraction
- fast reaction time ( $\mu\text{sec}$ )
- expert authorisation needed to change settings

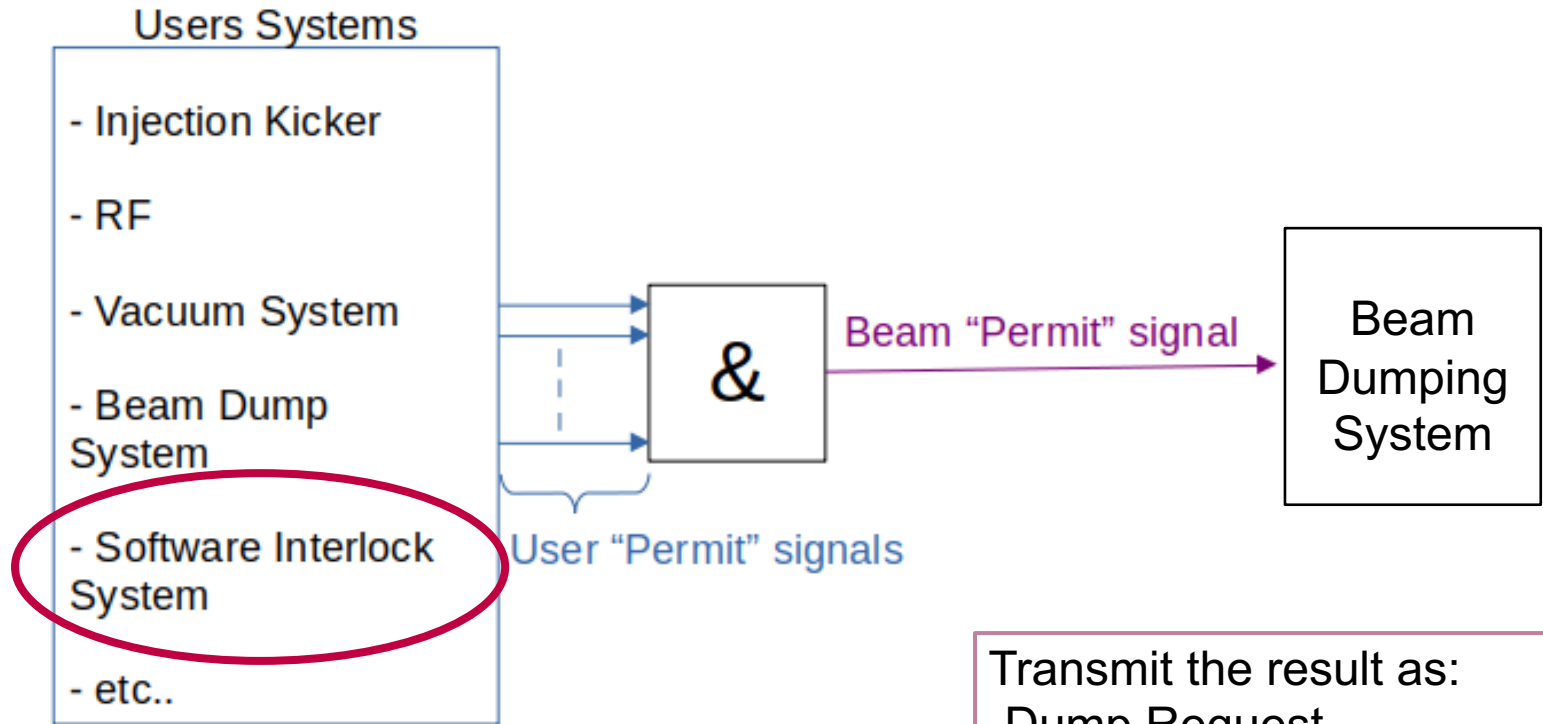
## Software Interlock System (SIS):

- entirely **Software** based
- more flexible
- implements more complex logic
- reaction time rather slow (hundreds msec)
- used also as a diagnostic tool
- SIS is a input of the BIS



# Beam Interlock System (BIS)

A number of users or clients provide interlock signals (permits) to the BIS.



The Beam Interlock System has to:

Collect status or default signals



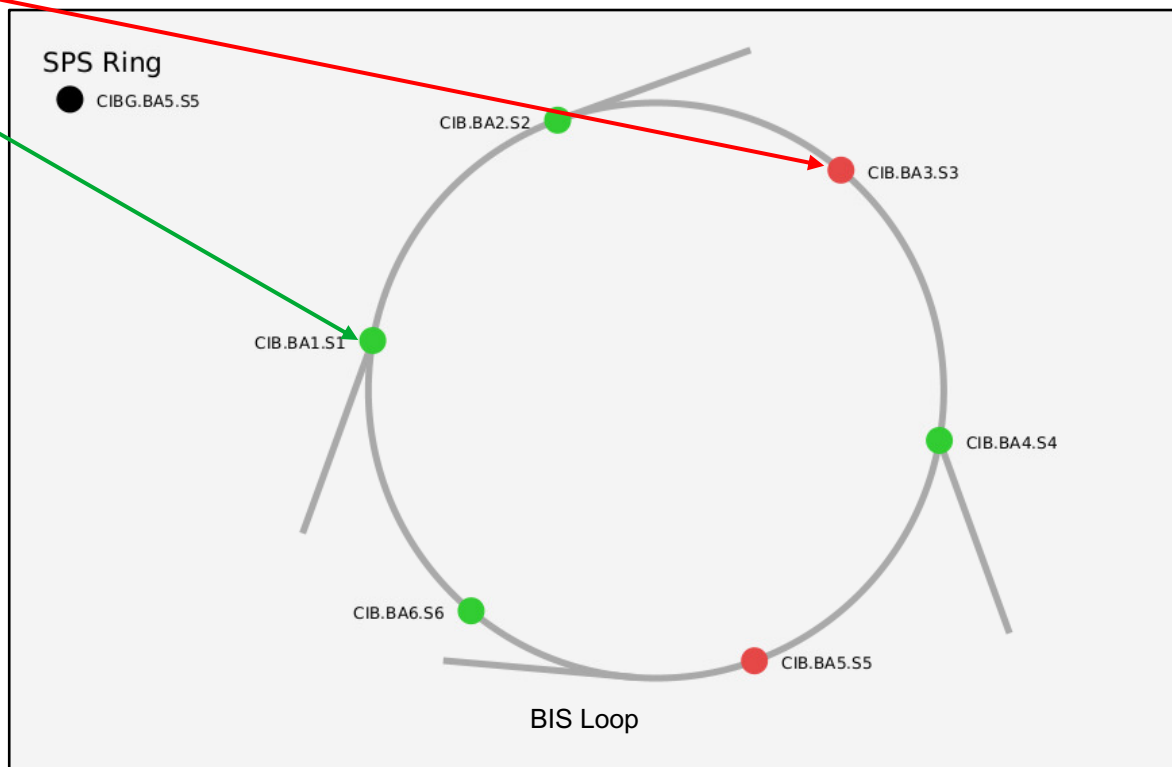
Perform a summation of all signals



Transmit the result as:  
-Dump Request  
-Inhibit of injection Permit  
-Inhibit of extraction Permit  
-OK for beam

The central element of the Beam Interlock System (BIS) is the Beam Interlock Controller (BIC). Each BIC acts as a local concentrator, collecting User Permit signals.

Permits may be **TRUE** or **FALSE**:



**TRUE** → No Interlock , Beam allowed

**FALSE** → Interlock present, Beam dumped



- **SIS monitors** ~ 9500 logic inputs, states and settings covering the SPS and its transfer lines for ~ 1045 devices.

- **SIS acquires** values, analyzes and converts into a logical state.

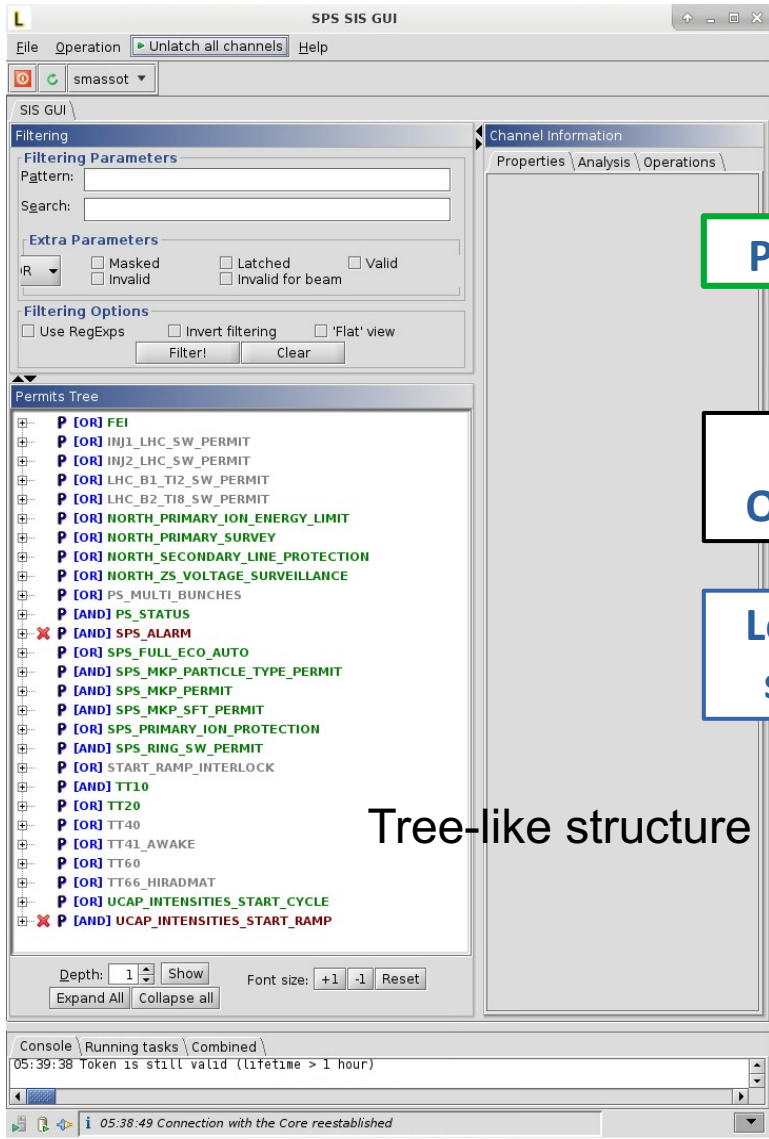
**TRUE** or **FALSE**

- The **SIS logical states** are grouped into tree-like structures and combined using logical operators.

**AND** or **OR**

□ **TRUE** : OK for beam operation.

□ **FALSE** : one or more tests indicate an abnormal situation.

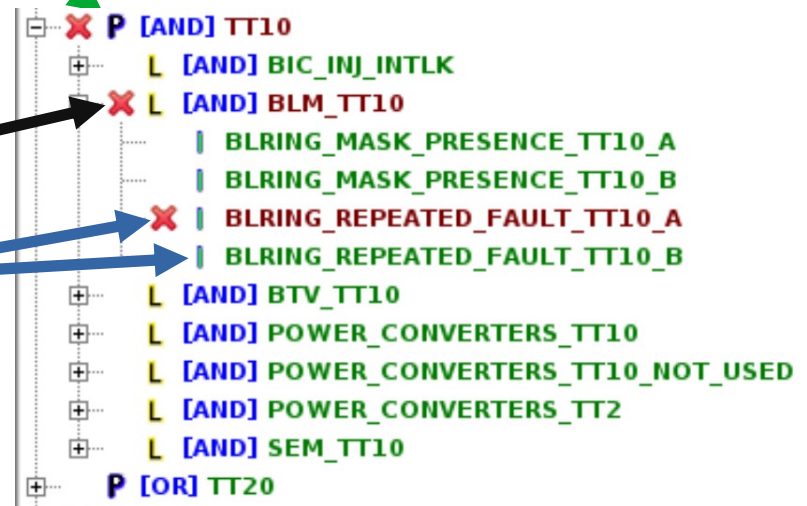


The top of the tree corresponds to a SOFTWARE PERMIT (SW\_PERMIT)

Permit

Logical Operator

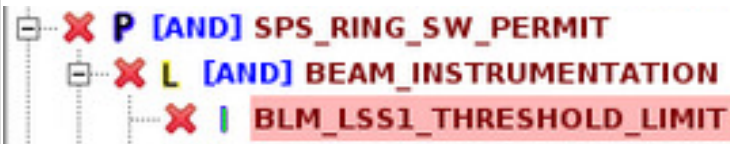
Logical state



Tree-like structure

# Used as diagnostic

Analysis



Properties \ Analysis \ Operations \ Analysis (SPS\_RING\_SW\_PERMIT.BEAM\_INSTRUMENTATION.BLM\_LSS1\_THRESHOLD\_LIMIT)

Actions Freeze

Events

- 10:32:49: Event on SPS.USER.MD1: SX.WECY-CT, counter=1, LATCHED
- 10:32:52: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:03: Event on SPS.USER.MD1: SX.WECY-CT, counter=1, LATCHED
- 10:33:07: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:18: Event on SPS.USER.MD1: SX.WECY-CT, counter=1, LATCHED
- 10:33:21: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:32: Event on SPS.USER.MD1: SX.WECY-CT, counter=1, LATCHED
- 10:33:36: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:46: Event on SPS.USER.MD1: SX.WECY-CT, counter=1, LATCHED
- 10:33:50: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED

Last errors in buffer:

- 10:33:21: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:36: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED
- 10:33:50: Event on SPS.USER.SFTPRO1: SX.WECY-CT, counter=0, FAILED on condition, LATCHED

Acquisition

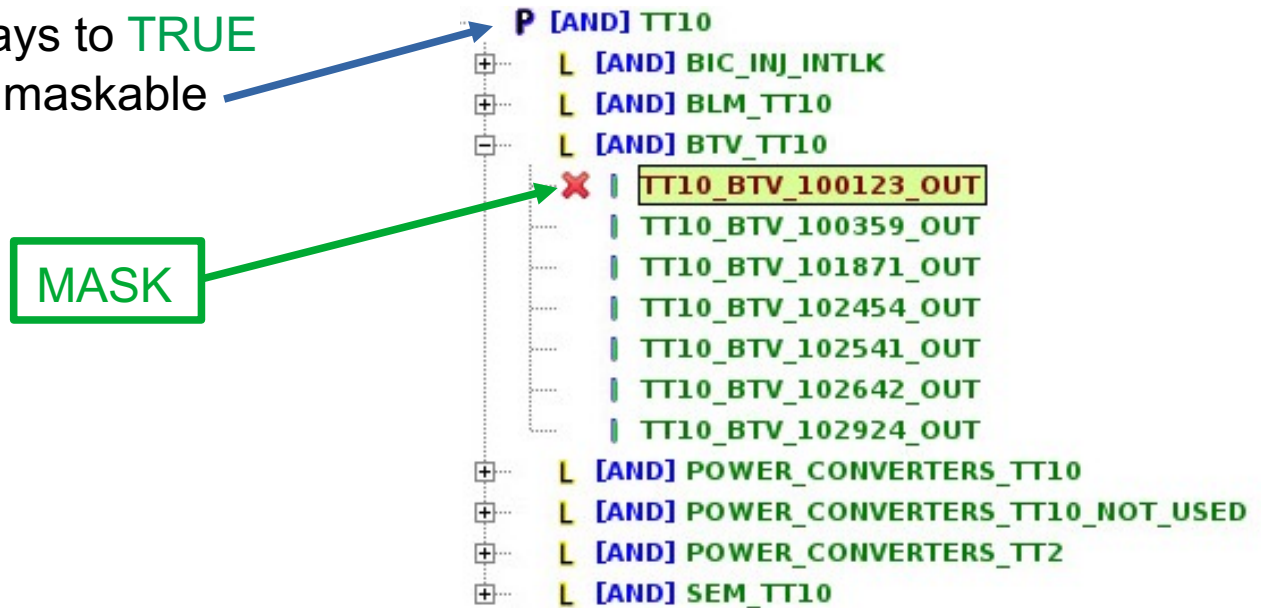
Reference

```
BLRSPS LSS1 ACQ == <Click for value>
rs3BeamDumpAllowedResume (boolean:1) -> true
signalSaturationThreshold_percent (float:1) -> 95.0
signalSaturationBeamDumpAllowedResume (boolean:1) ->
rs0MaxLosses_gray (float[:36] -> 0.0, 0.0, 0.0, 0.0,
hardwareGainOk (boolean:1) -> true
rs0BeamLossThresholds_gray (float[:36] -> 0.8101691,
rs5BeamDumpAllowedResume (boolean:1) -> true
rsBeamDumpAllowedOnAllChannelsAndAllRs (boolean:1) ->
interlockOk (boolean:1) -> true
rs0BeamDumpAllowedResume (boolean:1) -> true
```

```
SISREF LSS1 BLM THRESHOLDS == <Click for value>
result:
received from SISREF.LSS1/SISBlm#maxThreshold - Header
(float[:36]) -> 0.2, 1.6, 0.6, 0.6, 0.33, 0.4, 0.45, 0.7
```

## - Input Masking:

- Allows operators to ignore logical input
- Access privileges required
- Means evaluating always to **TRUE**
- Permit signals are not maskable

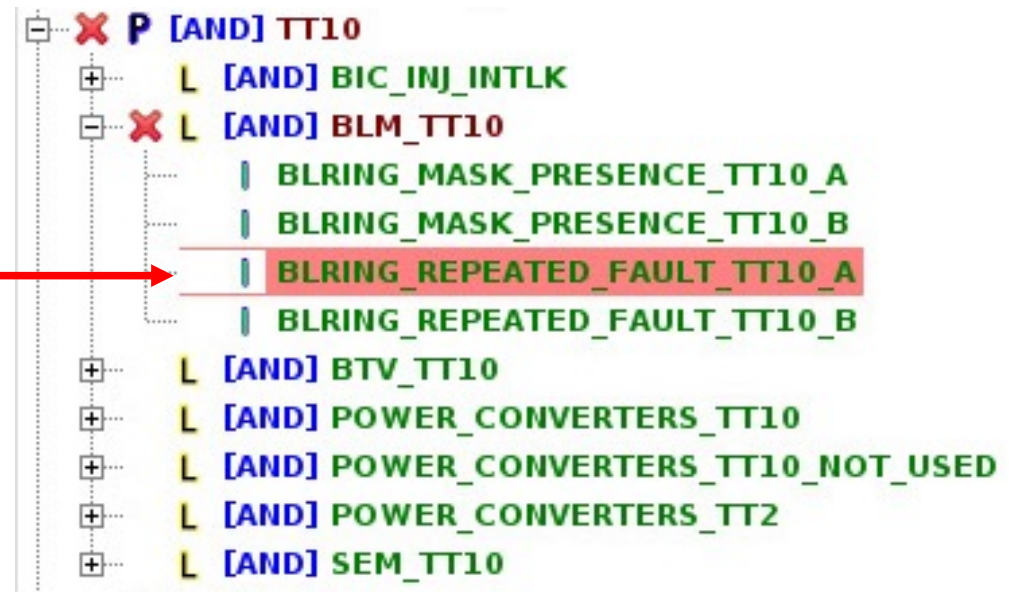


## - Input **Latching**:

- Situation that requires further investigation
- Must be manually unlatched

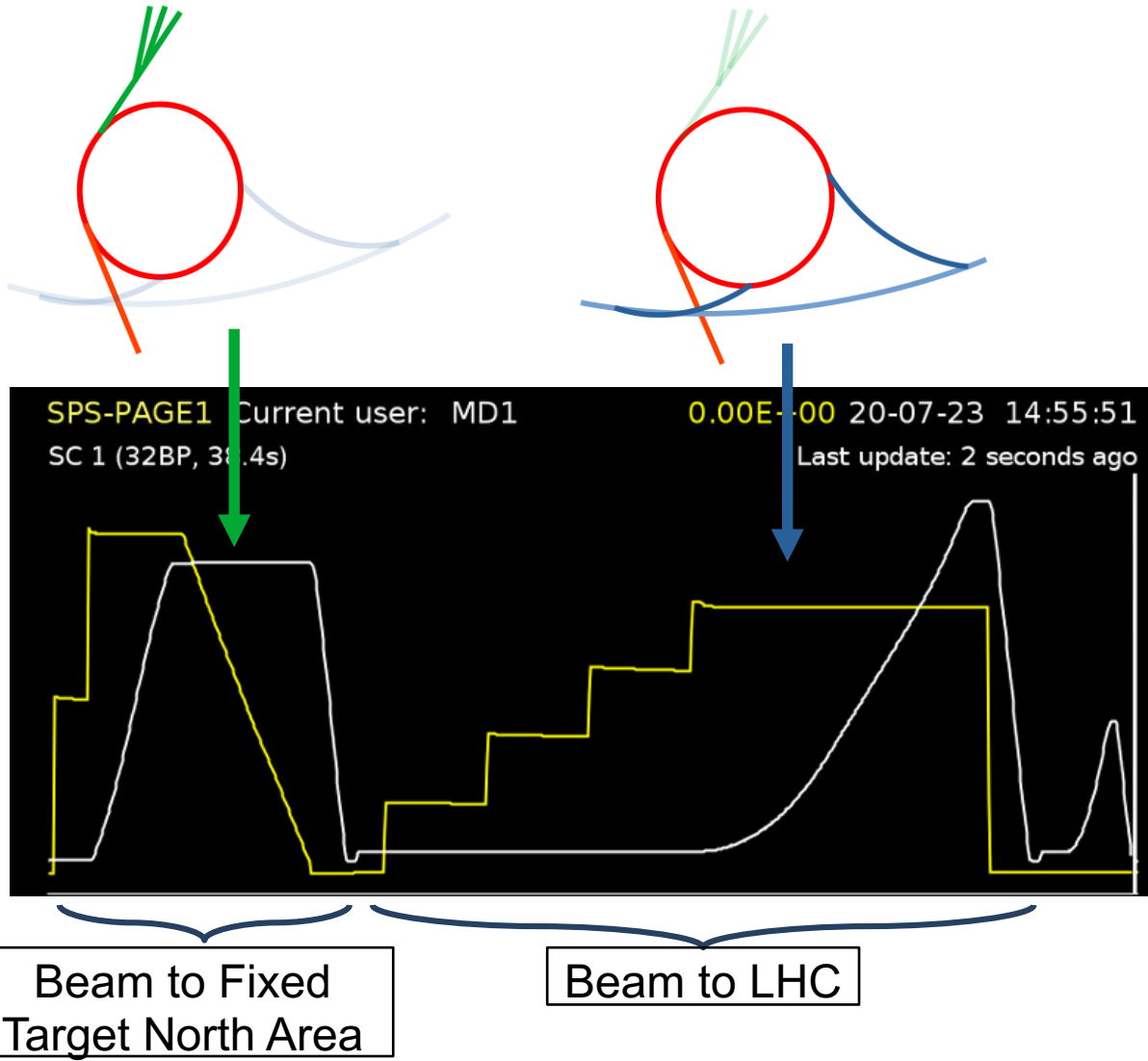
LATCH

▶ Unlatch all channels





SIS is structured by geographical zone (transfer lines, extractions, ring,...)



- SIS is a crucial tool for every day operation.
- SIS offers strong flexibility; changes can be made rapidly.
- The operations team carries out all changes related to the SIS.
- SIS complements the BIS, highlights configuration problems or minor issues.



One of the most powerful tools that guarantees  
a safe machine for reliable physics

ご清聴ありがとうございました

Thank you for your attention

Merci de votre attention

Special thanks to SPS team