MTCA workshop for accelerator and physics in Japan 2025

Thursday, 28 August 2025

<u>Session 3</u> - Bldg. #3 Seminar Hall (14:30 - 15:30)

time	[id] title	presenter
14:30	[14] Introduction of digitizers with MTCA interface and application specific firmwares	KAWAMATA, Yoshikazu
14:50	[13] A high-speed MTCA.4-based digitizer for the J-PARC MR	SUGIYAMA, Yasuyuki
15:10	[10] MMC Software Engineering: Challenges and Solutions	KITAGAWA, Ryuta