AC-coupled low-gain avalanche detectors (AC-LGADs) are being developed for high-precision timing measurements in high-energy physics experiments. Building on worldwide developments in LGAD technology, we have developed silicon detectors with finely segmented pixel readout enabled by AC coupling. This talk will present our AC-LGAD development, including ongoing work on dedicated readout ASICs and improvements in radiation tolerance. Beyond precision timing, we are also exploring the use of internal gain to reduce the power consumption of the readout electronics, offering a promising approach for future lepton collider detectors. Prospects for monolithic integration will also be briefly discussed.